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WORK EXPERIENCE

[2011 – Current] Head of Microelectronic Circuits and Systems Chair
European University of Armenia
Yerevan, Armenia

[2004 – Current] Director of Educational Department
Synopsys Armenia CJSC
Yerevan, Armenia

[2001– Current] Head of Microelectronic Circuits and Systems Chair,
National Polytechnic University of Armenia (NPUA),
Yerevan, Armenia

[2002– 2004] Director of Digital and I/O Design Department
LEDA Design Inc. ,
Yerevan, Armenia

[2001– 2004] VP of University and R&D Program,
Director of Educational Department LEDA Design Inc
Yerevan, Armenia

[1994– 2001] Vice Director of Graduate School
SEUA
Yerevan, Armenia

[1989– 1991] Scientific-research worker
Technical University of Vienna
Yerevan, Armenia

[1985– 2001] Associate Professor, Department of Computer Systems & Informatics
SEUA
Yerevan, Armenia

[1984– 1985] Associate Professor, Department of Computer Systems & Informatics
SEUA
Yerevan, Armenia

[1981– 1984] PhD student
Chair of Electronics of MIF,
Moscow, RF

[1978-1981] Assistant Professor,

Department of Computer Systems & Informatics
SEUA
Yerevan, Armenia

EDUCATION

[2016] Academician
Engineering Academy of Armenia
Yerevan, Armenia

[2014] Corresponding Member
National Academy of Sciences of the Republic of Armenia www.sci.am
Yerevan, Armenia

[2012] Academician
Academy of Sciences of Applied Radio Electronics of Russia, Ukraine and Belarus www.nure.ua
Ukraine

[2006] Full Professor in Automated Systems
SEUA, www.nuaca.am
Yerevan, Armenia

[2006] Doctor of Technical Sciences
SEUA, www.nuaca.am
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[1985] Associate Professor in Computer Science Supreme Certification Board
Moscow

[1984] PhD,
Moscow Engineering-Physics Institute (MIFI), www.mephi.ru
Moscow,

[1978] Diploma of Excellence in Computer Science,
Polytechnic Institute, www.polytech.am
Yerevan, Armenia

PUBLICATIONS / last 5 years /

[2022] Scientific publications: "Self-Heating Analysis Method of Integrated Circuits // International Conference on Microwave & THz Technologies and Optoelectronics ,Yerevan, Armenia,

[2022] Scientific publications: "A voltage control system for a low-power devices to address transistor overstress in I2C systems", Manual od Universities. ELECTRONICS, p. 374-381, Vol.27, N.3, ISSN 1561-5405,

[2022] Scientific publications: "A New, Effective Model of Industry", University Cooperation" Chapter in the book "Towards Third Generation Learning and Teaching. Contours of The New Learning" under the editorship of M. Yulik and H. Wissema , P. 307-320, Anthem Press, London,

[2022] Scientific publications: "Integrated Circuits: Evolution, Market, Challenges and Solutions", Proceedings of the 9th Small Systems Simulation Symposium 2022 (Plenary session), P.34-39,Niš, Serbia,

[2022] Methodical Publication: "Analog Custom Design Flow Tutorial" Instructional guidelines for laboratory works", p. 182, NPUA, Yerevan,

[2022] Methodical Publication: "Digital Integrated Circuits" Instructional guidelines for laboratory works" p. 157, NPUA, Yerevan,

[2022] Scientific publications: . "Integrated Circuit Reliability: Current State, Challenges and Solutions", Proceedings of the XIV International Symposium on Industrial Electronics and Applications (Plenary session), Banja Luka, Bosnia and Herzegovina,

[2022] Scientific publications: "Standard Cell Full Abutment Check Method", Proceedings of the 2022 IEEE 41st International Conference on Electronics and Nanotechnology (ELNANO), P.47-50,Kiev, Ukraine,

[2021] Methodical Publication: "Mixed-Signal IC Design" Instructional guidelines for a course project" , p. 24, NPUA, Yerevan,

[2021] Methodical Publication: "Hardware Description Languages" Instructional guidelines for laboratory works" p. 28, NPUA, Yerevan,

[2020] Monograph: "Peculiarities of Nanoscale Integrated Circuit I/O Cells Design" co-author p.240 Yerevan, Chartaraget,

[2020] Methodical Publication: "Analog Integrated Circuits" Instructional guidelines for laboratory works" p. 82, NPUA, Yerevan,

[2020] Methodical Publication: "Analog Integrated Circuits" Instructional guidelines for a course project, p. 20, NPUA, Yerevan,

[2018] Methodical Publication: "SAED 14nm Educational Design Kit Databook" p. 328 , NPUA, Yerevan,

[2018] Methodical Publication: “SAED 14nm Process Design Kit Databook”, p. 71, NPUA, Yerevan,

[2018] Monograph: “Integrated Circuit Design in Conditions of Uncertainties of Parameters” co-author p. 146, Yerevan, Chartaraget

[2018] Monograph: “Simulation and Optimization of Digital Circuits: Considering and Mitigating Destabilizing Factors”, p. 355, Springer International Publishing AG, part of Springer Nature,